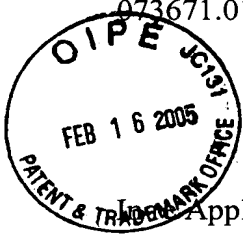


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10/052,063

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Jonathan Cheah et al.
Serial No.: 10/052,063
Filing Date: January 16, 2002
Group: 2133
Examiner: Christine Trinh Le Tu
Notice of Allowanced Mailed: December 16, 2004
Confirmation No.: 1470
Title: AN IN-CHIP MONITORING SYSTEM TO MONITOR
INPUT/OUTPUT OF FUNCTIONAL BLOCKS

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313.1450

Dear Sir:

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I hereby certify that this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37. C.F.R. 1.10 on the date shown below, and addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Name: Willie Jiles
Willie Jiles

Date: 2/16/2005

AMENDMENT UNDER RULE 37 C.F.R §1.312

The above-identified application has been carefully reviewed prior to the payment of the Issue Fee. It is requested that the following amendment be entered under the provisions of 37 C.F.R. §1.312.

IN THE SPECIFICATION

Please amend the Specification as follows:

On page 3, delete the current paragraph [0010]:

~~FIG. 4 is a diagram showing the configuration of multiple functional blocks wwithin a circuit, according to one embodiment of the invention;~~